



全漢企業股份有限公司

電氣規格書

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 作者 : 詹慧玲
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 製造商名稱 :
 Model No/Type :
 備註 : 1.增加延伸機種 FSP350-601U(S271) >>>9PA3501331>>>2003/11/14
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 研發部門 : RD2
 機密 : N
 編號 :
 發行日期 : 2003/11/17-10:50:44
 ISO 編號 :



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主管	莊同榮 Tj	文件發行	蕭雅鶴 Nora		

相關料號：

9PA3501300

FSP350-601U,ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),5L1S,FULL RANGE,RD2

9PA3501303

FSP350-601U(S031),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC,5L1S,INTEL,FULL RANGE,RD2

9PA3501304

FSP350-601U(S032),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),4L2S,INTEL,FULL RANGE,RD2

9PA3501305

FSP350-601U(S033),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),2L1S,INTEL,FULL RANGE,RD2

9PA3501306

FSP350-601U(F061),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP(F/S),W/PFC(A),5L1S,STD,INTEL,FULL RANGE,RD2

9PA3501307

FSP350-601U(S034),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),4L1S,INTEL,FULL RANGE,RD2

9PA3501308

FSP350-601U(S035),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),2L1S,INTEL,FULL RANGE,RD2

9PA3501309

FSP350-601U(S036),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),2L2S,INTEL,FULL RANGE,RD2

9PA3501310

FSP350-601U(S037),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),3L1S,INTEL,FULL RANGE,RD2

9PA3501311

FSP350-601U(S038),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),4L1S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501312

FSP350-601U(S039),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),4L2S,AMD,FULL RANGE,9PA3501303,RD2

9PA3501313

FSP350-601U(S03A),ATX,SPS,B,W/NK,W/IO,WO/SS,SPI,W/PFC(A),4L1S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501314

FSP350-601U(G061),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),4L2S,INTEL,FULL RANGE,9PA3501300,RD2

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9PA3501315

FSP350-601U(F062),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP(F/S),W/PFC(A),3L1S,INTEL,FULL RANGE,9PA3501306,RD2

9PA3501316

FSP350-601U(S03B),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),2L0S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501322

FSP350-601U(S03H),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),3L1S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501317

FSP350-601U(S03C),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),3L0S,INTEL,FULL RANGE,9PA3501304,RD2

9PA3501318

FSP350-601U(S03D),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),4L1S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501319

FSP350-601U(S03E),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),4L2S,INTEL,INTEL,FULL RANGE,9PA3501304,RD2

9PA3501320

FSP350-601U(S03F),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),2L1S,INTEL,FULL RANGE,9PA3501304,RD2

9PA3501321

FSP350-601U(S03G),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),2L2S,INTEL,FULL RANGE,9PA3501304,RD2

9PA3501323

FSP350-601U(S03I),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),1L0S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501324

FSP350-601U(C111),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),4L2S,OEM,INTEL,FULL RANGE,9PA3501300,RD2

9PA3501325

FSP350-601U(S03J),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),2L0S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501326

FSP350-601U(S03K),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),4L1S,INTEL,FULL RANGE,9PA3501303,RD2

9PA3501327

FSP350-601U(S03L),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,SPI,W/PFC(A),2L0S,INTEL,FULL RANGE,9PA3501325,RD2

9PA3501328

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FSP350-601U(S03M),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),4L1S,INTEL,
FULL RANGE,9PA3501304,RD2

9PA3501330

FSP350-601U(S03N),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,AIC,W/PFC(A),2L2S,INTEL,F
ULL RANGE,9PA3501304,RD2

9PA3501331

FSP350-601U(S271),ATX,SPS,B,W/NK,W/IO,WO/O,WO/SS,FSP,W/PFC(A),2L1S,INTEL,F
ULL RANGE





全漢企業股份有限公司
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SPECIFICATION

FSP350-601U **9PA3501300**

Main Feature:
Active PFC Circuit
Full Range Input

P.E	R/D	APPROVED	REV.
			5

DATE: October 29, 2003

表單編號: 7000-0105



全漢企業股份有限公司
SPI Electronic Co.,Ltd .

MODEL: FSP350-601U

Revision History

<u>Rev</u>	<u>Description</u>	<u>Date</u>	<u>Author</u>
4	1.Page 2---Revise 2.2 SAFETY	2003.09.01	Karl
5.	1.Page 3---Revise 2.2 SAFETY	2003.10.29	Joyhare

1. GENERAL DESCRIPTION AND SCOPE

This is the specification of Model FSP350-601U; AC-line powered switching power supply with active PFC (Power Factor Correction) circuit, PFC > 0.93 @110Vac. Designed and manufactured by FSP GROUP, INC. located in Taiwan, Republic of China.

The specification below is intended to describe as detailedly as possible the functions and performance of the subject power supply. Any comment or additional requirements to this specification from our customers will be highly appreciated and treated as a new target for us to approach.

2. REFERENCE DOCUMENTS

The subject power supply will meet the EMI requirements and obtain main safety approvals as following:

2.1 EMI REGULATORY

- FCC Part 15 Subpart J, Class 'B' 115 Vac operation.
- CISPR 22 Class 'B' 230 Vac operation.

2.2 SAFETY

- NEMKO EN 60950
- TUV EN60950 OR VDE EN60950
- CSA-C22.2 NO. 950-95
- IEC 60950
- UL 1950
- CE

3. PHYSICAL REQUIREMENTS

3.1 MECHANICAL SPECIFICATIONS

The mechanical drawing of the subject power supply, which indicate the form factor, location of the mounting holes, location, the length of the connectors, and other physical specifications of the subject power supply. Please refer to the attachment drawing.

3.2 CONNECTOR SPECIFICATIONS

The power supply connectors are:

- AC Inlet : Standard inlet socket 10A/250V, UL/CSA/VDE approved.
- P1 : The equivalent of MOLEX 39-01-2200, 20 pin connector
- PA,PB,PC,PD,PE : The equivalent of AMP 1-480424-0, 4 pin connector
- PF : The equivalent of AMP 171822-4, 4 pin connector
- PG : The equivalent of MOLEX 39-01-2040, 4 pin connector

3.3 CONNECTOR PIN DESIGNATIONS

The pin designations and color codes are defined as follows:

	P1 SYSTEM BOARD		PA,PB,PC,PD,PE DISK DRIVER		PF DISK DRIVER		PG DISK DRIVER	
PIN1	+3.3V	ORANGE	+12V	YELLOW	+12V	YELLOW	COM	BLACK
PIN2	+3.3V	ORANGE	COM	BLACK	COM	BLACK	COM	BLACK
PIN3	COM	BLACK	COM	BLACK	COM	BLACK	+12V	YELLOW
PIN4	+5V	RED	+5V	RED	+5V	RED	+12V	YELLOW
PIN5	COM	BLACK						
PIN6	+5V	RED						
PIN7	COM	BLACK						
PIN8	PW-OK	GRAY						
PIN9	+5Vsb	PURPLE						
PIN10	+12V	YELLOW						
PIN11	+3.3V	ORANGE						
	+3.3VS	BROWN						
PIN12	-12V	BLUE						
PIN13	COM	BLACK						
PIN14	PS-ON	GREEN						
PIN15	COM	BLACK						
PIN16	COM	BLACK						
PIN17	COM	BLACK						
PIN18	-5V	WHITE						
PIN19	+5V	RED						
PIN20	+5V	RED						

4. ELECTRICAL REQUIREMENTS

4.1 OUTPUT ELECTRICAL REQUIREMENTS

The subject power supply will meet all electrical specifications below, over the full operation temperature range and dynamic load regulation.

4.1.1. OUTPUT RATING

Output	Nominal	Regulation	Ripple/Noise	Min	Max
1	+3.3V	±5%	50mV	0.3A	20.0 A
2	+5V	±5%	50mV	2.0A	25.0 A
3	+12V	±5%	120mV	1.0A	20.0 A
4	-5V	±5%	100mV	0 A	0.3A
5	-12V	±10%	120mV	0 A	0.8A
6	+5VSB	±5%	100mV	0 A	2.0A

※ The +3.3V, +5V and +12V total output power shall not exceed 330W.

The +3.3V and +5V total output shall not exceed 157watts, and the total output for this subject power supply is 350 watts. Ripple and noise measurements shall be made under all specified load conditions through a single pole low pass filter with 20MHz cutoff frequency. Outputs shall bypassed at the connector with a 0.1uF ceramic disk capacitor and a 10uF electrolytic capacitor to simulate system loading.

4.1.2. LOAD CAPACITY SPECIFICATIONS

The cross regulation defined as follows, the voltage regulation limits DC include DC Output ripple & noise.

LOAD	STM.	+3.3V	+5V	+12V	-5V	-12V
ALL MAX	HHHHH	12.0A	18.0A	16.6A	0.3A	0.8A
+5V MAX other MIN	LHLLL	0.3A	25.0 A	1.0A	0A	0A
+3.3V MAX other MIN	HLLLL	20.0 A	2.0 A	1.0A	0A	0A
+12V MAX other MIN	LLHLL	0.3A	2.0 A	20.0A	0A	0A
ALL MIN	LLLLL	0.3 A	2.0 A	1.0A	0A	0A

4.1.3. HOLD-UP TIME (@FULL LOAD)

115V / 60Hz : 17 mSec. Minimum.

230V / 50Hz : 17 mSec. Minimum.

The output voltage will remain within specification, in the event that the input power is removed or interrupted, for the duration of one cycle of the input frequency. The interruption may occur at any point in the AC voltage cycle. The power good signal shall remain high during this test.

4.1.4. OUTPUT RISE TIME

(10% TO 90% OF FINAL OUTPUT VALUE, @FULL LOAD)

115V-rms or 230V-rms + 5Vdc : 20ms Maximum

4.1.5.OVER VOLTAGE PROTECTION

+5V_{dc} output: +5.58V_{dc} minimum, + 6.82V_{dc} maximum

+ 3.3V_{dc} output: +3.5V_{dc} minimum, + 4.5V_{dc} maximum

+12V_{dc} output: +13.4V_{dc} minimum, +15.6V_{dc} maximum

4.1.6.SHORT CIRCUIT PROTECTION

Output short circuit is defined to be a short circuit load of less than 0.1 ohm.

In the event of an output short circuit condition on +3.3V, +5V or +12V output, the power supply will shutdown and latch off without damage to the power supply. The power supply shall return to normal operation after the short circuit has been removed and the power switch has been turned off for no more than 2 seconds.

In the event of an output short circuit condition on -5V or -12V output, the power supply will not be damaged. The power supply shall return to normal operation as soon as the short circuit has been removed. and the power switch has been turned off for no more than 2 seconds.

4.1.7.OVERLOAD PROTECTION

Overload currents defined as a 10 amp/sec fault current ramp starting from full load, applied to the +3.3V, +5V output, shall not cause that output to exceed 60amps before the output voltage drops below 0.5 volts and is latched off. The +12V output shall not exceed 35 amps under the same ramp conditions starting at full load before it is latched off.

The overload protection must be such that the power supply is protected from damage by entering a shutdown condition.

4.1.8.POWER GOOD SIGNAL

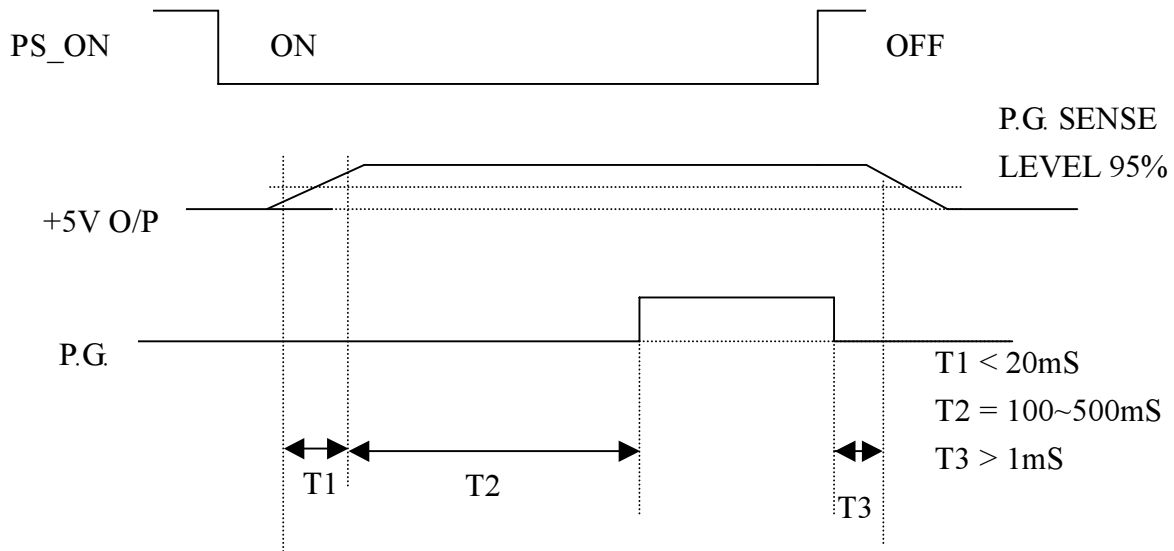
The power good signal is a TTL compatible signal for the purpose of initiating an orderly star-up procedure under normal input operating conditions. This signal is asserted (low) until +5Vdc has reached 4.75 volts during power up. Characteristics:

TTL signal asserted (low state) : less than 0.5V while sinking 10mA.

TTL signal asserted (high state): greater than 4.75V while sourcing 500uA.

High state output impedance: less or equal to 1Kohm from output to common.

POWER GOOD @ 115/230V, FULL LOAD	100 –500mSec.
POWER FAIL @115/230V, FULL LOAD	1 mSec. minimum



4.2. OUTPUT TRANSIENT LOAD RESPONSE

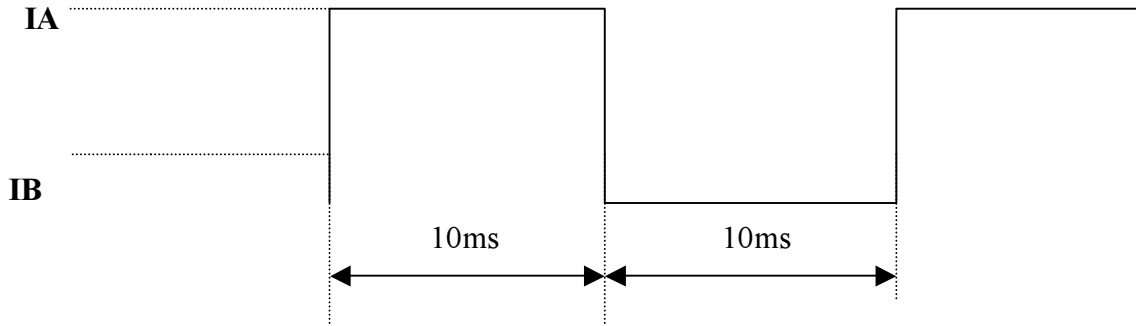
+5V and +12V must be within specification for a step change in current as specified below. The outputs will be tested one section at a time with all other sections at maximum load. The test transition will be from IA to IB and IB to IA. The step current will have a nominal transition time of 0.5 amp per microsecond for +5V and 0.1 amp per microsecond for +12V.

+5V vdc:

IA: 25.0 amps
 IB: 18.75 amps
 Volts variation: 400 mV max (p-p)
 Setting time: 10 ms max

+12V vdc:

IA: 20.0 amps
 IB: 15.0 amps
 Volts variation: 450 mV max (p-p)
 Setting time: 10 ms max



4.3. INPUT ELECTRICAL SPECIFICATIONS

4.3.1. VOLTAGE RANGE

PARAMETER	MIN.	NOM.	NAX.	UNITS
V-in Range	90	115/230	264	V-rms

4.3.2. INPUT FREQUENCY

NOMINAL FREQUENCY	
115V	60Hz
230V	50Hz

4.3.3. INRUSH CURRENT

(Cold start – 25 deg. C)

115V	60 Amps - peak
230V	90 Amps - peak

4.3.4. INPUT LINE CURRENT

115V	6.0 Amps – rms maximum
230V	3.0 Amps – rms maximum

4.4. EFFICIENCY

115 VAC @Full Load	65% minimum
230 VAC @Full Load	65% minimum

4.5. PS_ ON#

PS_ ON# is an active-low, TTL-compatible signal that allows a motherboard to remotely control the Power supply in conjunction with features such as soft on/off, Wake on LAN+, or wake-on-modem.

When PS_ON# is pulled to TTL low, the power supply should turn on the five main DC output rails: +12VDC,+5VDC,+3.3VDC,-5VDC,and -12VDC. When PS_ON# is pulled to TTL high or open-circuited, the DC output rails should not deliver current and should be held at zero potential with respect to ground. PS_ON# has no effect on the +5VSB output, which is always enabled whenever the AC power is present. Table 15 lists PS_ON# signal characteristics.

The power supply shall provide an internal pull-up to TTL high. The power supply shall also provide debounce circuitry on PS_ON# to prevent it from oscillating on/off at startup when activated by a mechanical switch. The DC output enable circuitry must be SELV-compliant.

Table 15. PS_ON# Signal Characteristics

	Min.	Max.
V _{IL} , Input Low Voltage	0.0V	0.8V
I _{IL} , Input Low Current (V _{in} = 0.4V)		-1.6mA
V _{IH} , Input High Voltage (I _{in} = -200 μA)	2.0V	
V _{IH OPEN} circuit, I _{in} = 0		5.25V

5. ENVIRONMENTAL REQUIREMENTS

The power supply will be compliant with each item in this specification for the following Environmental conditions.

5.1. TEMPERATURE RANGE

Operating	350W	50 deg. C
Storage	-20 to +80 deg.C	

5.2. HUMIDITY

Operating	5 -95% RH, Non-condensing
Storage	5 -95% RH, Non-condensing

5.3. VIBRATION

The subject power supplies will withstand the following imposed conditions without experiencing non-recoverable failure or deviation from specified output characteristics.

Vibration Operating – Sine wave excited, 0.25 G maximum acceleration, 10-250 Hz swept at one octave / min. Fifteen minute dwell at all resonant points, where resonance is defined as those exciting frequencies at which the device under test experiences excursions two times large than non-resonant excursions.

Plane of vibration to be along three mutually perpendicular axes.

5.4. SHOCK

The subject power supplies will withstand the following imposed conditions without experiencing non-recoverable failure or deviation from specified output characteristics.

Storage -40G, 11 mSec. half-sine wave pulse in both directions on three mutually perpendicular axes.

Operating -10G, 11mSec. half-sine wave pulse in both directions on three mutually Perpendicular axes.

5.5 COOLING SPECIFICATIONS

The subject power supply is used by two of 4 cm ball-bearing fan for cooling.

6. SAFETY

6.1. LEAKAGE CURRENT

In addition to the UL CSA and NEMKO safety requirements, the leakage current from AC to safety ground will not exceed 0.75 mA-rms at 115Vac, 60 Hz.

7. ELECTROMAGNETIC COMPATIBILITY

7.1 LINE CONDUCTED EMI

The subject power supplies will meet FCC and VFG class B requirements under all conditions.

7.2. RADIATED EMI

The subject power supplies will meet FCC and CISPR 22 requirements under all load conditions.

8. LABELLING

Label marking will be permanent, legible and complied with all agency requirements.

8.1. MODEL NUMBER LABEL

Labels will be affixed to the sides of the power supply showing the following:

- Manufacturer's name and logo.
- Model no., serial no., revision level, location of manufacturer.
- The total power output and the maximum load for each output.
- AC input rating.

8.2 DC OUTPUT IDENTIFICATION

Each output connector will be labeled.

9. RELIABILITY

9.1. MTBF

The power supply have a minimum predicted MTBF(MIL-HDBK-217) of 100,000 hours of continuous operation at 25°C, maximum-output load, and nominal AC input voltage.